

PRODUCT SPECIFICATION

Customer	
Project	
Part No.	Z28005-P40-I941-Y1
Remarks	□APPOVAL FOR SPECIFICATION ONLY ■APPOVAL FOR SPECIFICATION AND SAMPLE

	CUSTOME	₹	Z	HUNYIKE	П
Approved	Checked	Prepared	Approved	Checked	Prepared
Ву	Ву	Ву	Ву	Ву	Ву
				Chi Tin	Yang
				Shi Jin	Zifeng



Revision Record

Rev. No.	Date	Description
V1.0	2023-03-03	Preliminary Specification Release.

Contents

1. General Specifications	3
2. Interface Definition Description	4
3. Mechanical Drawing	5
4. Electrical Specifications	6
5. Delivery Inspection	10
6. Reliability Test	12
7. Precautions	13
8. Packing and Storage	15

1. General Specifications

No.	Item	Specification	Unit
1	Display Size (Diagonal)	2.8	inch
2	Display Resolution	240(H) × RGB × 320 (V)	pixels
3	Pixel Pitch	180(H) × 180 (V)	um
4	LCM Outline Dimension (Without FPC)	50 (W) ×69.20 (H) ×2.30 (T)	mm
5	LCD Outline Dimension	46.20 (W) × 63.88 (H) × 0.8 (T)	mm
6	LCD Active Area	43.20 (W) ×57.60 (H)	mm
7	View Direction (Gray Inversion)	FULL VIEW	-
8	Driver IC	ILI9341V	-
9	Pixel Arrangement	RGB-Stripe	-
10	Display Mode	Mode Normal Black	
11	Pixel driving Element	a-Si TFT	
12	LCD Transmittance	Typ.: 4.8% Min:4.5%	
13	LCD Contrast Ratio	Typ.: 1200 Min:1000	-
14	FPC Version	Z28005-P40 V1	-
15	Interface	ee MCU/SPI	
16	Operating Temperature	-20°C∼ 70°C	-
17	Storage Temperature	-30°C∼ 80°C	-
18	Backlight Arrangement	LED/3 Series 2 Parallel (6 lights in total)	-
19	Luminance	Тур.:700	
20	Weight	TBD	kg



2. Interface Definition Description

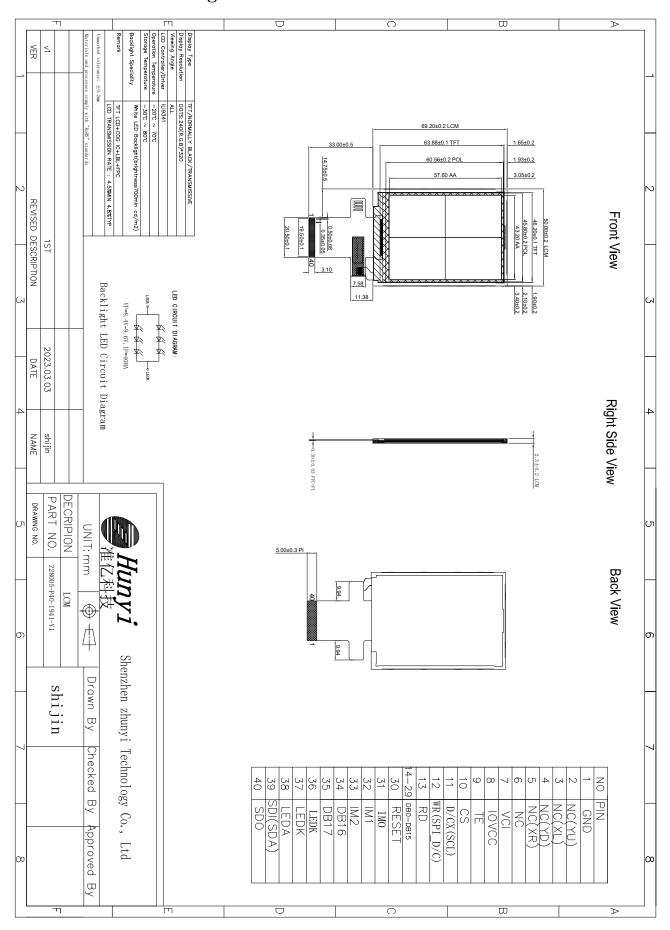
PIN NO.	PIN DEF.	FUNCTION DESC.
1	GND	Ground
2	NC(YU)	No Connection(Touch Panel Up Side)
3	NC(XL)	No Connection(Touch Panel Left Side)
4	NC(YD)	No Connection(Touch Panel Down Side)
5	NC(XR)	No Connection(Touch Panel Right Side)
6	NC	No Connection
7	VCI	POWER SUPPLY
8	IOVCC	I/O POWER SUPPLY
9	TE	Tearing Effect PIN
10	CS	Chip Select PIN
11	D/CX(SCL)	Data/Command Control PIN(SPI clock (SCL) clock (SCLK))
12	WR(SPI_DC)	Write enable in 8080 MCU parallel interface(SPI_Data/Command Control PIN)
13	RD	Read enable in 8080 MCU parallel interface
14-29	DB0-DB15	DATA BUS
30	RESET	LCM RESET PIN
31	IM0	Interface Select Pin
32	IM1	Interface Select Pin
33	IM2	Interface Select Pin
34	DB16	DATA BUS
35	DB17	DATA BUS
36	LEDK	POWER SUPPLY- FOR BACKLIGHT CATHODE
37	LEDK	POWER SUPPLY- FOR BACKLIGHT CATHODE
38	LEDA	POWER SUPPLY- FOR BACKLIGHT ANODE
39	SDI(SDA)	SPI data input
40	SDO	SPI data output

RESET voltage should be consistent with IOVCC voltage, or there probably is black screen fault when power on.

11.40	IM1 IM0 MCU-Interface Mode	11.40	DB Pin in u	se	
IM2	IIVI I	IIVIU	MCU-interface Mode	Register/Content	GRAM
0	0	0	80 MCU 8-bit bus interface I	D[7:0]	D[7:0]
0	0	1	80 MCU 16-bit bus interface I	D[7:0]	D[15:0]
0	1	0	80 MCU 9-bit bus interface I	D[7:0]	D[8:0]
0	1	1	80 MCU 18-bit bus interface I	D[7:0]	
1	0	1	3-wire 9-bit data serial interface I	SDA: In/OUT	
1	1	0	4-wire 8-bit data serial interface I	SDA: In/OUT	



3. Mechanical Drawing



4. Electrical Specifications

4.1. LCD Optical Charcteristics

Item		Symbol Conditions		Specification			Unit	Note
		Symbol	Conditions	Min.	Тур.	Max.	Unit	Note
Transmittance (V	With PL)	T(%)	Viewine	4.5	4.8	-	%	-
Contrast Ratio		CR	Viewing normal angle $x = y = 0$	1000	1200	-	-	-
Response Time		TR+TF		-	30	35	ms	-
	11	Өх+		75	80	-		
Wissering Amele	Hor.	Өх-	CR≥10	75	80	-		
Viewing Angle	V	Өу+	at 25℃	75	80	-	deg.	-
	Ver.		75	80	-			

4.2. Electrical Specifications

Item	Symbol	S	Unit		
Item	Symbol	Min.	Тур.	Max.	Umt
Power For Analog Circuit	AVDD	-	-	-	V
TFT Gate On Voltage	VGH	-	14	-	V
TFT Gate Off Voltage	VGL	-	-12	-	V
TFT Common Electrode Voltage	Vcom	-	-	-	V

4.3. Typical Operating Conditions

Item	Symbol	Min.	Тур.	Max.	Unit
Analog Supply Voltage	VCI	2.5	2.8	3.3	V
I/O Supply Voltage	IOVCC	1.65	1.8	3.3	V
Input High Voltage	VIH	7.8 × IOVCC	-	IOVCC	V
Input Low Voltage	VIL	GND	-	0.3 × IOVCC	V



Output High Voltage	VOH	0.8 × IOVCC	-	IOVCC	V
Output Low Voltage	VOL	GND	-	0.2 × IOVCC	V

4.4. Backlight Circuit Specifications

Item		Symbol	Min.	Тур.	Max.	Unit	Test Condition
Current	Current		-	40	-	mA	-
Voltage		$V_{\rm f}$	8.4	9.0	9.6	V	-
LCM Unifor	mity	-	80	-	-	%	10.40
Life Tim	e	-	30000	-	-	Hr.	- If=40mA
Power Consur	nption	PBL	-	360	-	mW	
		Rx	-	-	-		
	Red	Ry	-	-	-		
7.00.6		Gx	-	-	-		
LCM	Green	Gy	-	-	-		Average the brightness
Chromaticity Coordinate	Blue	Bx	-	-	-		EV at 9 points, Optical
		Ву	-	-	-		- Instrument BM-7
		Wx	-	-	-		
	White	Wy	-	-	-		

4.5. LCD Power Consumption

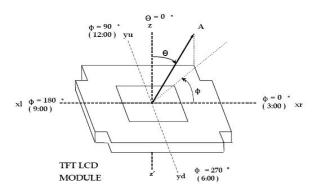
Mode	Symbol	Тур.	Max.	Unit	
Normal Mode	VCI+IOVCC	-	-	mA	
Test Condition: VCI=2.8V.					
Interface Drive Type: row flipping or column flipping.					
IPS Type LCD Panel => All Black Pattern.					
TN Type LCD Panel => All White Pattern.					
Temperature: 25°C.					
Mode	Symbol	Тур.	Max.	Unit	



Sleep Mode	VCI+IOVCC	-	-	μΑ	
Test Condition: VCI=2.8V.					
DC/DC converter is enabled. Internal oscillator is started and panel scanning is started.					
Except for the IC internal crystal oscillator and panel scanning, other functions are suspended.					
Temperature: 25°C.					

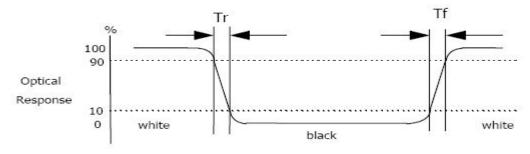
4.6. Measuring System

4.6.1. LCM Viewing Angle



Viewing angle is the angle at which the contrast ratio is greater than 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface.

4.6.2. Response Time



Response time is the time required for the display to transition from white to black (Rising time, Tr) and from black to white (Falling time, Tf) for additional information.

4.6.3. Contrast Ratio (CR)

Contrast Ratio (CR) is defined mathematically as:

Contrast Ratio = $\frac{\text{Surface Luminance with all white pixels}}{\text{Surface Luminance with all black pixels}}$

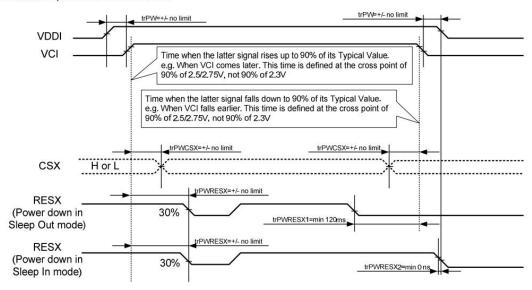
Surface luminance is the center point across the LCD surface 500mm from the surface with all pixels displaying white.

4.7. Power On / Power Off

4.7.1. Power On/Off

12.1. Case 1 – RESX line is held High or Unstable by Host at Power ON

If RESX line is held High or unstable by the host during Power On, then a Hardware Reset must be applied after both VCI and VDDI have been applied – otherwise correct functionality is not guaranteed. There is no timing restriction upon this hardware reset.

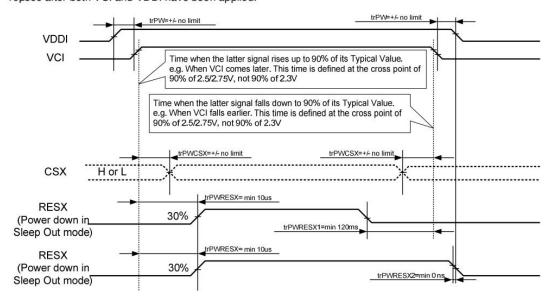


trPWRESX1 is applied to RESX falling in the Sleep Out Mode trPWRESX2 is applied to RESX falling in the Sleep In Mode

Note 1: Unless otherwise specified, timings herein show cross point at 50% of signal power level.

12.2. Case 2 – RESX line is held Low by Host at Power ON

If RESX line is held Low (and stable) by the host during Power On, then the RESX must be held low for minimum 10µsec after both VCI and VDDI have been applied.



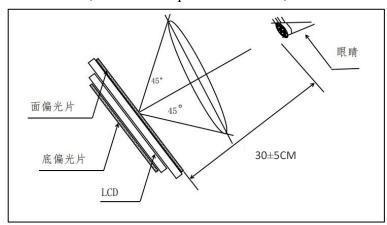
trPWRESX1 is applied to RESX falling in the Sleep Out Mode trPWRESX2 is applied to RESX falling in the Sleep In Mode

Note 1: Unless otherwise specified, timings herein show cross point at 50% of signal power level.

5. Delivery Inspection

5.1. Quality Inspection Environmental Conditions

- 5.1.1. Viewing distance: the normal viewing distance between the screen and the inspector is 30±5cm; Inspection Angle: 90°±45° (90° indicates that the inspector's perspective is perpendicular to the product to be inspected).
- 5.1.2. Visual inspection illumination: 1000±200LUX;Electrical inspection illumination: 200±100LUX;Ambient temperature 25±5°C, ambient humidity 55±15%RH.



5.2. Quality Inspection Standard

No.	Defect		Standard	Defect Grade	Result
	Spot Defect (including bright	< 7 inches	Φ ≤ 0.10mm	Ignore	OK
			$0.10 \text{mm} < \Phi \le 0.20 \text{mm}$ $DS \ge 10 \text{mm}$	Minor Defect	OK
			Φ > 0.20mm	Serious Defect	NG
1	1 spot / color spot / bubble / dark spot, etc.)		$\Phi \le 0.15$ mm	Ignore	OK
		≥ 7 inches	$0.15 \text{mm} < \Phi \le 0.25 \text{mm}$ $DS \ge 10 \text{mm}$	Minor Defect	OK
			Φ > 0.25mm	Serious Defect	NG
		Φ: defect diamet	ter. DS: spacing.		'
2	Linear Defect	< 7 inches	W≤0.02mm,	Ignore	OK
	(scratches,	V menes	L: unlimited	ignore	



	filaments, etc.)		$0.02\text{mm} < W \le 0.03\text{mm}$ $L \le 5\text{mm}$		Minor Defect	OK
			W > 0.0	03mm	Serious Defect	NG
			$W \le 0.0$	03mm	Ignore	OK
			L: unli	mited	-5	
		≥ 7 inches	$0.03 \text{mm} < W \le 0.05 \text{mm}$		Minor Defect	OK
			L≤5mm			
			W > 0.0	05mm	Serious Defect	NG
		W: defect width	. L: defect length. D	S: spacing.		
		Display Area	Judge by S	pot Defect		
3	Polarizer Bubble	Black Edge	The distance from	_	Minor defect	OK
		Area	The distance from the edge of the display area is less than 0.5mm.		Judge by Spot Defect	
4	Polarizer Bump (Mark)	Display Area / Black Edge Area	Invisible when the touch screen or cover plate is assembled.		Minor Defect	OK
		Item	Method	Instrument	Median	Tolerance Range
				0 1 1	According to the	
5 Color and Luminance		Color and Coord	x, y Color	Optical	actual test on the	± 0.04
	Color and		Coordinate	Instrument BM-7	sample confirmed	
	Luminance			DIVI-/	by the customer.	
		Luminance	Average the	Average the Optical Instrument	According to the	± 20%
			brightness EV at		actual test on the	
			9 points BM-7	sample confirmed		
			•		by the customer.	
6	6 Other Standards Subject to the negotiation by both parties.					



7	Warranty Period	One year after sale.		
8	Guarantee	ROHS, REACH		
9	Websites	Official: https://en.zhunyikeji.com/ Globle Resources: https://zhunyikeji.en.alibaba.com/ Alibaba: https://zhunyikeji.en.alibaba.com/ 1688: https://shop9641057ru80o3.1688.com/		

6. Reliability Test

Item	Condition	Result Determination
High-Temperature Storage	80°C 120H	
Low-Temperature Storage	-30°C 120H	After the test, leave the LCD
High-Temperature Operation	70°C 120H	samples indoors at normal
Low-Temperature Operation	-20°C 120H	temperature and humidity for 2H for function and
High-Temperature and High-Humidity	60°C 85%RH 120H	appearance inspection. The sample should meet the
Thermal Cycling Test	-20 °C/0.5H $\sim +70$ °C/0.5H 10 cycles in total	requirements on electrical performance, but be free from
Vibration Test	Frequency: 10Hz ~ 55Hz ~ 10Hz Amplitude: 0.75mm X, Y, Z direction for 1h in total (Packing Condition)	the following defects: 1. Air bubble in the module, 2. No display, 3. Glass crack.
ESD Test	± 600 V Human Body Mode 150pF/330Ω ± 800 V Air Mode 150pF/330Ω	

Note:

- 1) Each module under test can only be used for one of the test items.
- 2) The quantity of samples for each test item is 2.
- 3) Fault Judgment Criterion: Basic Specifications, Electrical Specifications, Mechanical Specifications, Optical Specifications.



7. Precautions

- 7.1. The display screen consists of glass and polarizer. Since the glass is fragile, the user must pay special attention to the edge area, and protect it from falling, vibration, or mechanical impact.
- 7.2. If the display screen is damaged and the liquid crystal material leaks, be sure not to get any in the mouth. If the liquid crystal material contacts the skin or clothes, flush off with soap and water.
- 7.3. Do not apply excessive force to the display screen or the joint part, or the color will change. Do not touch the display screen with bare hands, which will stain the display area and degraded insulation between terminals (some of the appearance is determined by the polarizer).
- 7.4. The polarizer covering the display panel of the LCD module is soft and easy to be scratched, be sure to handle carefully. Do not touch, impact, press, or rub the exposed polarizers with anything harder than an HB pencil lead (e.g.: glass, tweezers, etc.). Do not place or attach anything onto the display area to avoid leaving marks. The condensed material on the surface or terminals due to cold will damage or stain the polarizer. After the test in low temperature environment, the product must be warmed up in a container before put into the room temperature environment.
- 7.5. If the display panel is stained, blow warm air onto the surface and gently wipe it with a soft and dry cloth. If it is seriously contaminated, wipe it with a wet cloth dipped in one of the following solvents:
 - glycerol
 - ethyl Alcohol

Do not scrub, and avoid damaging the display panel.

- 7.6. Solvents other than those mentioned above may damage the polarizer. In particular, never use any of the following solvents:
 - water
 - ketone
 - arene

Wipe off saliva or water drop immediately, the contact with water over a long period of time may cause deformation or color fading. Avoid contact with oil or grease.

- 7.7. Special note: minimize electrode corrosion. Because electrode corrosion can be accelerated by water droplets, condensation of humidity, or electrification in a high humidity environment.
- 7.8. Assemble the LCD Module by the mounting holes. Make sure the LCD module make sure there is no bending, distortion, or deformation. Do not forcibly pull or bend the transmission wire or the backlight wire.

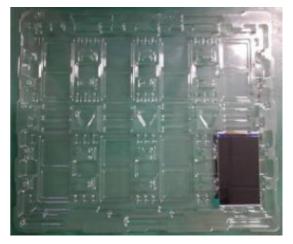


- 7.9. Do not disassemble the LCD module.
- 7.10. NC terminal should be disconnected. Do not connect any device.
- 7.11. If the logic circuit power supply is off, do not send the input signal.
- 7.12. Since the LCD module is integrated with CMOS, pay special attention to the modules. To prevent electrostatic damage, be careful to maintain an suitable work environment.
 - Make sure the module has the same potential as the human body before take the LCD module out
 of the packing box for assembly. The reliable grounding is necessary during module processing.
 - The required tool, such as the electric soldering iron, must be reliably grounded. Make sure the it is connected to AC power supply, and no electric leakage. When fixing the module with electric screwdriver, it must be grounded, to reduce the electromagnetic wave generated by the electric commutator spark as much as possible.
 - Do not assemble or operate under dry condition to reduce the static electricity. To reduce static electricity, the workplace must not be too dry. The recommended relative humidity is 50 60%.
 Keep your work clothes and work table grounded as much as possible
 - The LCD module is coated with a film to protect the display surface. Be careful when peeling off
 the film to reduce the generated static electricity.
- 7.13. Since the LCD module has high precision assembly and regulation, try to avoid excessive impact on the module or making any changes:
 - Do not change the shape of the tab on the metal frame.
 - Do not drill any extra hole, modify the shape, or change the position of component on the printed circuit board.
 - Do not change or damage the pattern on the printed circuit board.
 - Never modify the zebra strip (conductive rubber) or heat seal connector.
 - Do not make any change with the electric soldering iron except for the joint.
 - Do not throw, bend or twist.

8. Packing and Storage

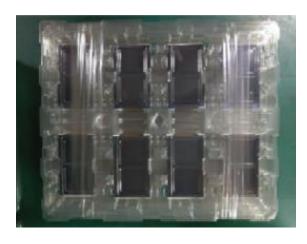
8.1. Packing Method

Step 1



Take 1pcs of the product, put it into a anti-static bag.

Step 2



Take 2 bags of product to place into the carton, make sure they are surface to surface. Put a piece of EPE pad between the carton and the separator to protect the products.

Step 3



Put the products into cartons one by one, each carton contains 60 pieces of products.

Step 4



The cartons should be taped and shipped with labels.

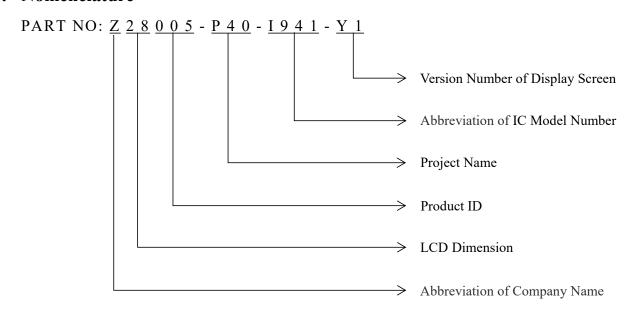
8.2. Storage Method

Store in an ambient temperature of 23±5°C, and in a relative humidity of 60±15%. The storage period should not exceed 12 months. Do not expose to the sun for a long period of time.

- 8.2.1. Store in clean environment, free from dust, active gas, or solvent.
- 8.2.2. Store in anti-static environment.



8.3. Nomenclature



8.4. Label

